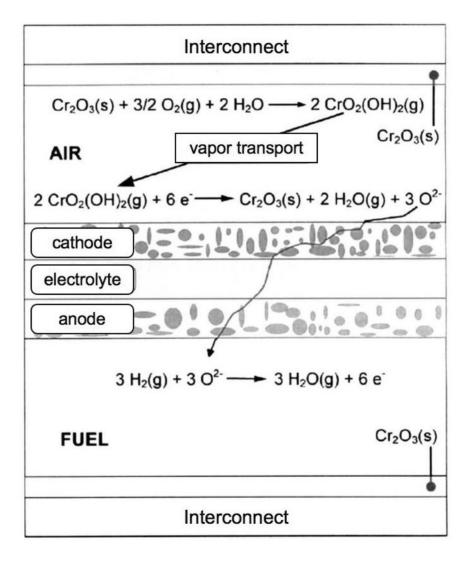
#### **Cathode-Interconnect Interfacial Properties**

Edgar Lara-Curzio, Yanli Wang, Amit Shyam, Rosa Trejo, Beth Armstrong, John Henry, Claire Chisholm, Tom Watkins

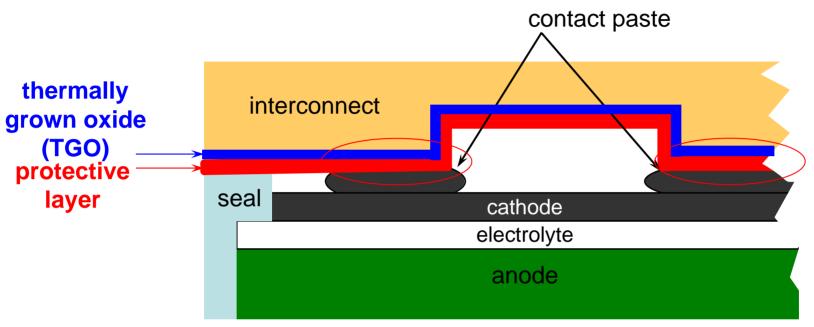
Materials Science & Technology Division
Oak Ridge National Laboratory
Oak Ridge, TN 37831-6062

9<sup>th</sup> Annual SECA Workshop Pittsburgh, PA August 5-7, 2008

### Background – Chromium poisoning



### Background - Cathode side interfaces



- Protective layer has several requirements\*
  - diffusion barrier, thermomechanical/chemical stability etc
- Complex system with several interfaces
- Degradation may be related to debonding due to thermal cycling

<sup>\*</sup> Wang et al., Surface and Coating Technology, 2006

# Approach

- Phase ID and phase evolution in protective layer during processing\*
  - Processing includes reduction and re-oxidation (specimen preparation - PNNL)
  - Phase transitions effect the stress development in layers
  - Phases in thermally grown oxide (TGO) identified
- Fracture Characterization of the interconnect-cathode interfaces as a function of processing conditions
  - Processing includes reduction, re-oxidation and pO<sub>2</sub> cycling\*\*
  - Effect of sintering temperature, volume fraction of pore former and geometric parameters
  - Energy release rate as a function of processing conditions

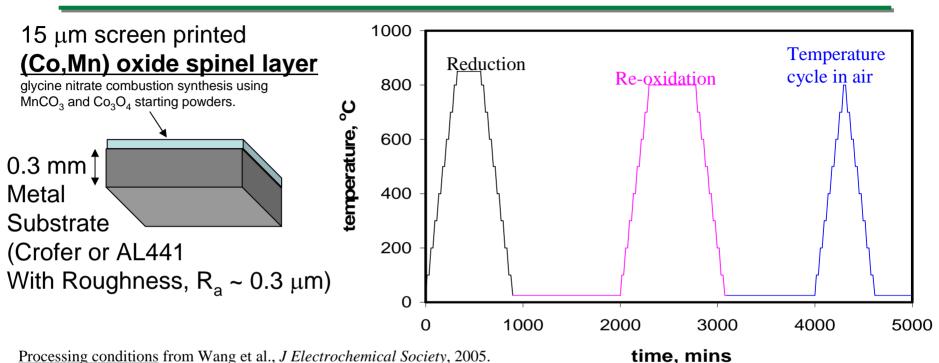


<sup>\*</sup> Wang et al., J Electrochemical Society, 2005.

<sup>\*\*</sup> McCarthy et al., J Power Sources, 2008.

# Phase Evolution Study

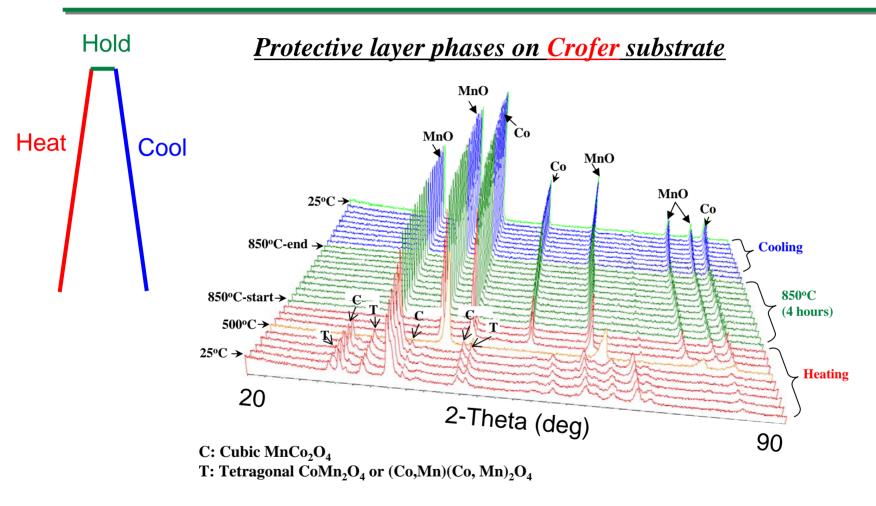
# In-situ XRD on protective layer



Processing conditions from Wang et al., J Electrochemical Society, 2005.

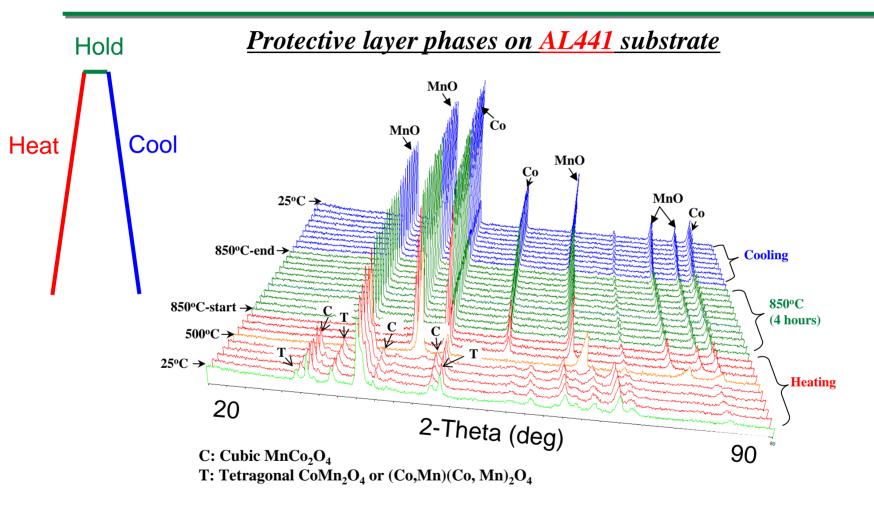
	Reduction	Oxidation
Heating/Cooling Rate	5 °C/min	5 °C/min
Measurements during heating/cooling	Every 100 °C (20mins scan)	Every 100 °C (20mins scan)
Holding temperature/time	850 °C (4 hours)	800 °C (8 hours)
XRD scan time during holding	20 mins	20 mins
Environment	Wet 4%H <sub>2</sub> 96%N <sub>2</sub> (13.3 cc/min)	Air

# Results – Phase evolution for <u>reduction</u> cycle



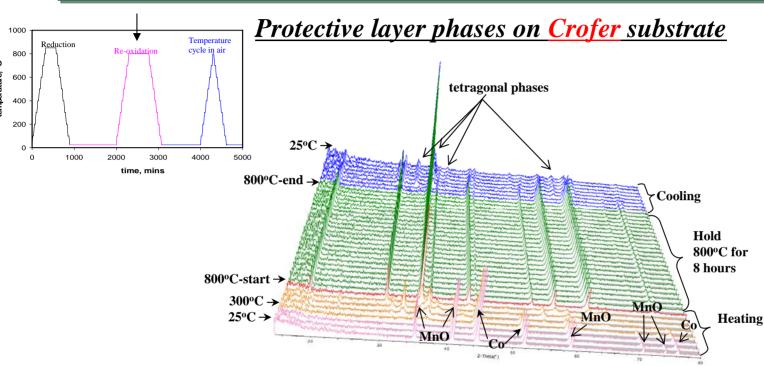
- C + T oxide phases until 400 °C → Transition state 400 600 °C → Co + MnO
- Co + MnO are present at the end of the reduction process

# Results – Phase evolution for <u>reduction</u> cycle



• The evolution of the phases in AL441 is the same as the Crofer substrate

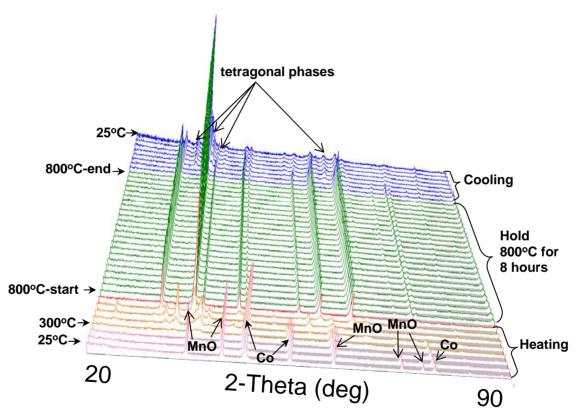
## Results – Phase evolution for <u>re-oxidation</u> cycle



- Co and MnO start oxidizing at 300 °C
- $400 500 \text{ °C} \text{Co} \rightarrow \text{CoO} + \text{Co}_3\text{O}_4$ ; MnO (c)  $\rightarrow \text{Mn}_2\text{O}_3$  (c)  $\rightarrow \text{Mn}_3\text{O}_4$  (t)
- 500 °C phases  $\rightarrow$  Tetragonal CoMn<sub>2</sub>O<sub>4</sub> or (Co,Mn)(Co, Mn)<sub>2</sub>O<sub>4</sub> + Co<sub>3</sub>O<sub>4</sub> (c) + Mn<sub>2</sub>O<sub>3</sub> (c) + Mn<sub>2</sub>O<sub>4</sub> (o)
- 700 °C and above only cubic MnCo<sub>2</sub>O<sub>4</sub> is present
- During cooling tetragonal phase appears at 600 °C and cubic and tetragonal spinel phases co-exist below 500 °C

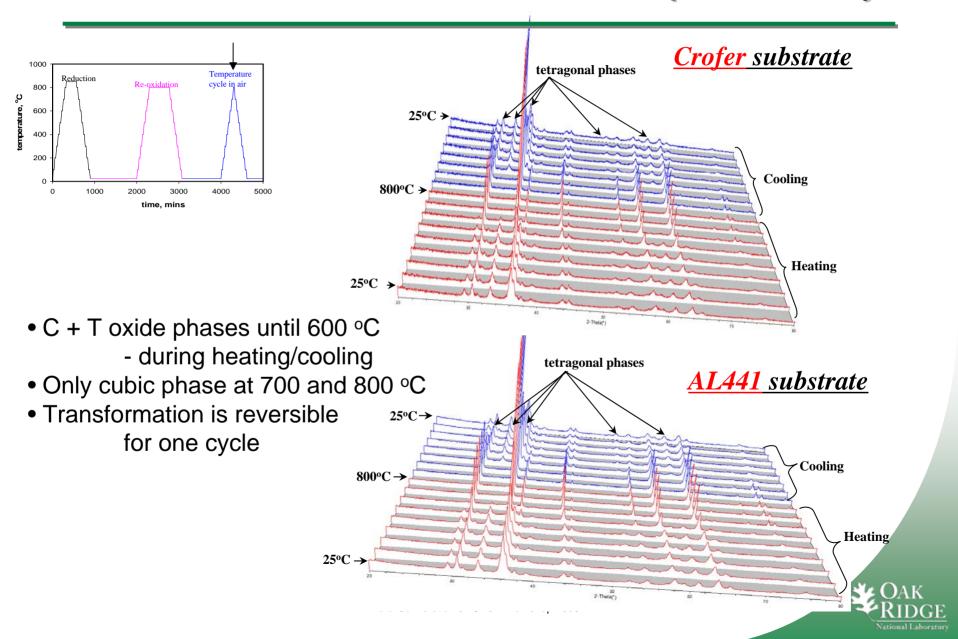
# Results – Phase evolution for <u>re-oxidation</u> cycle

#### Protective layer phases on AL441 substrate

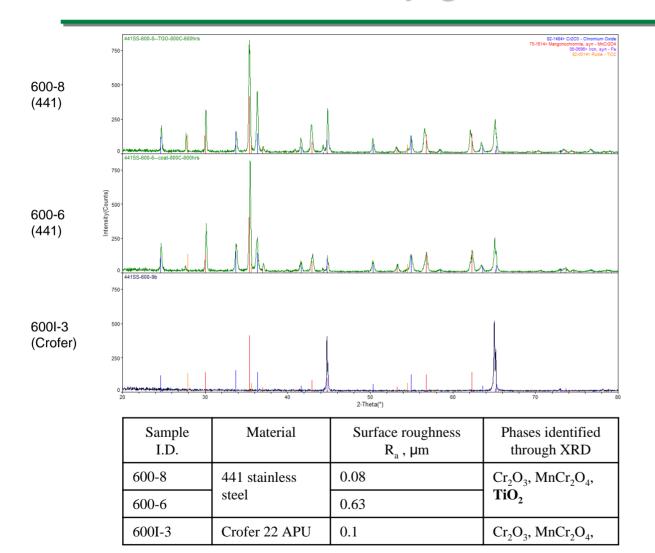


- The evolution of the phases during re-oxidation in AL441 is the same as evolution of the phases on Crofer substrates
- The phase transformations would affect the stress development inside the layers

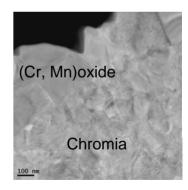
# Results – Phase evolution for Temperature cycle



# Phases in thermally grown oxide (TGO) layer



Treatment 800 °C for 600 hours



Phases confirmed by TEM analysis (for Crofer)

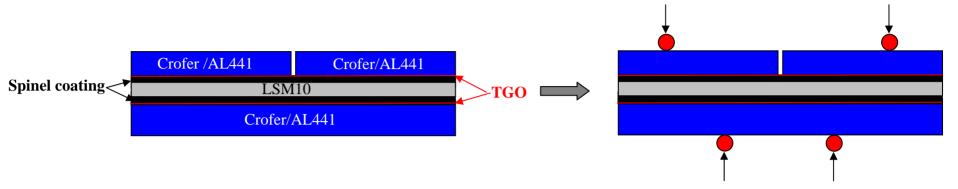
Titanium oxide present on Al441 but not on Crofer

# Summary - Phase evolution for protective layer

- The phase transformations in the spinel protective layer due to reduction, re-oxidation and temperature cycling were identified.
- Under oxidizing conditions, the cubic spinel MnCo<sub>2</sub>O<sub>4</sub> is stable at elevated temperatures whereas both cubic and tetragonal phases co-exist at lower temps.
- The phase evolution is important to quantify the residual stresses in the layers.
- Residual stress measurements are in progress at NSLS.

### Interfacial Characterization

### Sample preparation and testing



- AL441, Crofer22
  - Cut and ground to either 30 or 15 mm in length and 300 µm in thickness with high values of flatness and parallelism
- 15 μm spinel protective layer screen printed
  - Same reduction and re-oxidation procedure as described for phase ID study
- Commercial LSM paste screen printed
  - Sandwich specimen sintered with total LSM layer thickness 60 70 μm
  - Variables Sintering conditions (air/cyclic pO<sub>2</sub>), temperature, concentration of pore former, thickness of LSM, aging and thermal cycling.

# Calculation of Strain Energy Release Rate

Four point bending based interfacial fracture energy calculation\*

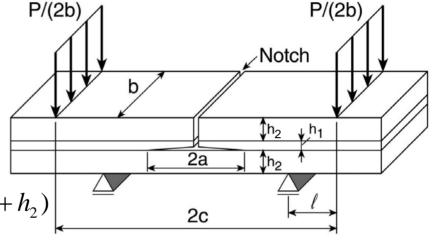
$$G = \frac{(1 - v^2)M^2}{2E_2} \left(\frac{1}{I_2} - \frac{1}{I_c}\right)$$

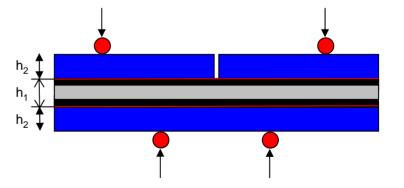
$$M = Pl/2b$$

$$I_c = kh_1^3 / 12 + 2h_2^3 / 3 + h_1h_2(h_1 / 2 + h_2)$$

$$k = \frac{E_1(1 - v_2^2)}{E_2(1 - v_1^2)}$$

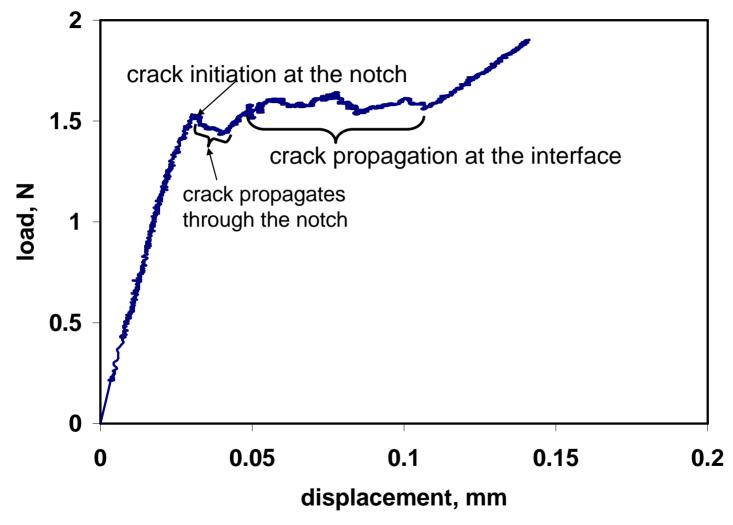
$$I_2 = h_2^3 / 12$$





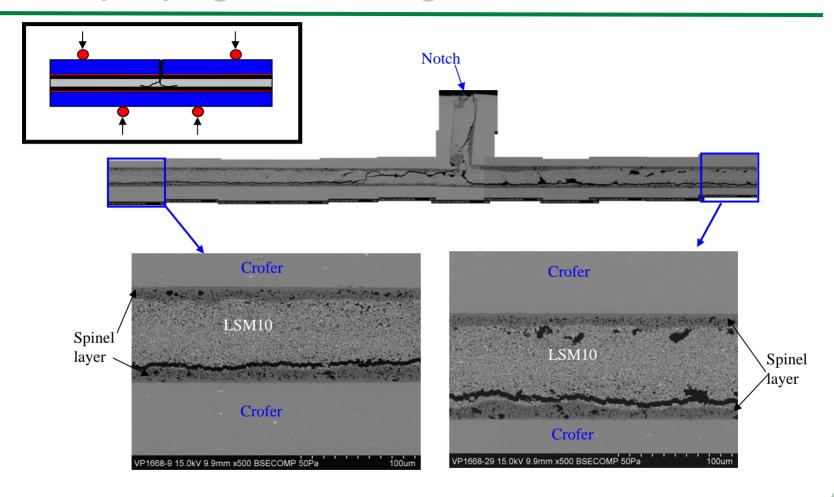
<sup>\*</sup> Hofinger et al., International Journal of Fracture 92: 213-220, 1998.

### Example load-displacement behavior



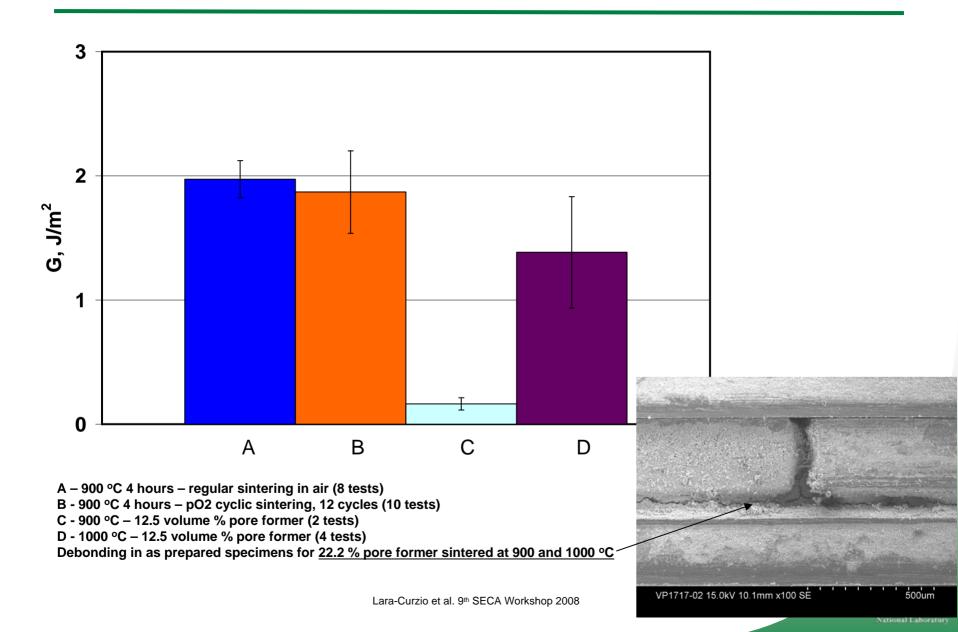
Steady state load gives a measure of the strain energy release rate of interface

# Crack propagation along interface - SEM



- Crack propagation path is the interface between the LSM10 and spinel protective layer or within the LSM10 layer near the interface
- Mechanical behavior is characterized with test method

#### Strain energy release rate variation



#### **Summary – Interfacial Toughness**

- A methodology has been established to determine the fracture behavior of metallic interconnects and LSM contact paste interfaces.
- The crack propagation path is identified as the interface between the LSM10 and spinel protective layer or within the LSM10 layer near the interface.
- Preliminary test results show that pO<sub>2</sub> cycling sintering does not influence the interfacial toughness (t-test at the 95% confidence level indicates the difference between the mean values is not significant).

#### **Summary**

- Phase evolution in the screen printed spinel protective layer and its implications for the mechanical behavior were identified.
- Interfacial toughness testing methodology was developed and implemented.

#### **Acknowledgments**

Our Friends at PNNL: Gary Yang, Larry Pederson and Liz Stephens

James Rakowski – Allegheny Ludlum

This research was sponsored by the US Department of Energy, Office of Fossil Energy, SECA Core Technology Program at ORNL under Contract DEAC05-00OR22725 with UT-Battelle, LLC. Program managers Travis Shultz and Wayne Surdoval.

This presentation was prepared as an account of work sponsored by an agency of the United States Government. Neither the United States Government nor any agency thereof, nor any of their employees, makes any warranty, express or implied, or assumes any legal liability or responsibility for the accuracy, completeness, or usefulness of any information, apparatus, product, or process disclosed, or represents that its use would not infringe privately owned rights. Reference herein to any specific commercial product, process, or service by trade name, trademark, manufacturer, or otherwise does not necessarily constitute or imply its endorsement, recommendation, or favoring by the United States Government or any agency thereof. The views and opinions of authors expressed herein do not necessarily state or reflect those of the United States Government or any agency thereof."